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Recently, the neural network has been applied to calibrate the successive-approximation-register and pipelined analog-to-digital converters (SAR-Pipelined ADCs), which requires no prior knowledge.

A Partially Binarized and Fixed Neural Network Based Calibrator for SAR-Pipelined ADCs Achieving 95.0-dB SFDR

Does RISC-V processor verification provide common ground to develop a new verification methodology, and will that naturally lead to new and potentially open tools?

New Methodologies Create New Opportunities

Collaboration Brings Integrated and Validated IP, Design Tools and

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Methodology to Facilitate Low Power, High-performance Mobile System-on-Chip Design MOUNTAIN VIEW, Calif., Jan. 17, 2011-- Synopsys ..

Synopsys Announces Production-Ready Lynx Design System Optimized for Common Platform 28-nm High-K Metal Gate Technology

Nanya Technology replaces its incumbent custom design tools with Synopsys Custom Design Platform to streamline memory design flow and improve team productivity Full flow, including Synopsys Custom ...

Synopsys Delivers Enhanced Memory Design Productivity to Nanya Technology

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Engineering Software (CAD, CAM, CAE, AEC, & EDA) Market Is Booming Worldwide | Dassault Systems, IBM, Ansys

You can add assertions to your RTL code as you write it " these form "active comments" that document what you have written ... The courses include full support for Synopsys tools, including Design ...

Using SystemVerilog Assertions in RTL Code

IP market leader ARM was able to reverse its decline in a year where the market grew strongly, according to Eric Esteve from IPnest.

ARM reverses decline in boom year for IP

Artificial intelligence (AI) technologies have dramatically advanced in recent years, resulting in revolutionary changes in people's lives. Empowered by edge computing, AI workloads are migrating

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from ...

Enabling Design Methodologies and Future Trends for Edge AI: Specialization and Co-design

Both the ARC SEM130FS Processor and tRoot HSM for Automotive are supported by comprehensive safety documentation ... Synopsys has a long history of being a global leader in electronic design ...

Synopsys Extends DesignWare Security, Processor IP Solutions to Address Safety and Security Requirements of Automotive Designs

"Our collaboration with Synopsys and the deployment of Fusion Compiler delivers accelerated access to this highly optimized technology, enabling our mutual customers to design and deploy market ...

Arm Leverages Synopsys Fusion Compiler to Enable Best PPA for Latest Neoverse Platforms

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Synopsys, Inc. (Nasdaq: SNPS) today announced it has completed its acquisition of MorethanIP, a provider of Ethernet Digital Controller IP supporting data rates from 10G to 800G. This acquisition ...

Synopsys Completes Acquisition of MorethanIP

Synopsys Design Compiler NXT now available Synopsys is aiming to provide faster runtime and improved quality of results with the announcement of its Design Compiler NXT. The release comes with ...

SD times news digest: Sublime Text 3.2, dotData 1.4, and GraphQL Foundation's collaboration with the Joint Development Foundation

When you use variables in C, the compiler associates a particular location in memory (by address) with the variable name you give it. Pointers let you get in the middle of this association — and ...

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The Basics And Pitfalls Of Pointers In C

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The Globe and Mail

From better type checking and compiler errors messages to Python-like string handling and plans to replace the #include system, there's a lot at play here! As a language, being more liberal and ...

C++20 Is Feature Complete; Here's What Changes Are Coming

A month has gone by since the last earnings report for Synopsys (SNPS ... Continuum Platform and Fusion Compiler product within Fusion Design Platform was a major growth driver in the first ...

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Why Is Synopsys (SNPS) Down 16.1% Since Last Earnings Report?

Cling accepts partial input and ensures that the compiler process keeps running to act on code as it comes in. It includes an API providing access to the properties of recently compiled chunks of ...

C++ Interpreter Cling Embraces Python Interoperability and Jupyter Notebooks

Its long history goes back to Excel's formulae (now getting an upgrade into a full-blown programming language in their own right) and Access's web-application design tools, through the drag-and ...

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